IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

DOCKET NO.: BUR919990222US2

Examiner: A. TUGBANG

Art Unit: 3729

n re Application of: ERIC ADLER 999999 Serial No.: 10/057,185 Filed: **JANUARY 25, 2002** For: METHOD OF MAKING **CAPACITOR HAVING SIDEWALL** SPACER PROTECTING THE § DILECTRIC LAYER

LETTER TO THE OFFICIAL DRAFTSMAN

DRAWING REVIEW BRANCH

U. S. Patent and Trademark Office Alexandria, Virginia 22313-1450

Sir:

Submitted herewith are five (5) sheets of formal drawings replacing the informal drawings filed initially with the above-referenced allowed patent application.

Respectfully submitted,

11/8/05

Andrew J. Dillon

Registration No. 29,634

DILLON & YUDELL LLP

8911 N. Capital of Texas Hwy., Suite 2110

Austin, Texas 78759

(512) 343.6116

ATTORNEY FOR APPLICANT